III B. Tech I Semester Supplementary Examinations, May - 2016 PULSE AND DIGITAL CIRCUITS

SET - 1

(Common to ECE and EIE)

Time: 3 hours Max. Marks: 70

Note: 1. Question Paper consists of two parts (Part-A and Part-B) 2. Answering the question in **Part-A** is compulsory 3. Answer any **THREE** Questions from **Part-B** PART -A 1 Describe about ringing circuit. [3M] a) b) State clamping circuit theorem. [4M] Explain piecewise linear characteristics of a diode. c) [4M] d) What are the types of triggering? Distinguish between them. [4M] e) What are the applications of Time-base generator? [3M] f) What do you mean by sampling gate? Give the applications of sampling gate? [4M] PART-B 2 Discuss about attenuators. a) [4M] Analyze the low pass circuit for the exponential inputs, with help of waveforms. [8M] b) Discuss the application of an attenuator in a CRO. c) [4M] 3 Draw the basic circuit diagram of positive peak clamper circuit and explain its [8M] a) operation. b) Explain transfer characteristics of emitter coupled clipper and derive necessary [8M] equations. 4 Draw the circuits of 3-input OR-gate using diodes for: [8M] (i) Positive logic, (ii) Negative logic and explain the operation of circuit. Give the comparison of different logic families. b) [8M] 5 What are different types of multivibrators? Explain the stable state of a multivibrator. a) [8M] Sketch the circuit diagram of Schmitt trigger and explain its operation. [8M] b) 6 With the help of neat circuit diagram and waveforms explain transistor miller time [8M] base generator. b) Discuss about the recovery time of a sweep circuit. How do you achieve short [8M] recovery time? 7 Explain the process of synchronization of a sweep circuit. [8M] a) Write notes on: [8M] i) Astable relaxation circuits ii) Monostable relaxation circuits
