# III B. Tech I Semester Supplementary Examinations, May - 2016 <br> PULSE AND DIGITAL CIRCUITS 

(Common to ECE and EIE)
Time: 3 hours
Max. Marks: 70

> Note: 1. Question Paper consists of two parts (Part-A and Part-B)
> 2. Answering the question in Part-A is compulsory
> 3. Answer any THREE Questions from Part-B
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> PART -A

1 a) Describe about ringing circuit.
b) State clamping circuit theorem.
c) Explain piecewise linear characteristics of a diode.
d) What are the types of triggering? Distinguish between them.
e) What are the applications of Time-base generator?
f) What do you mean by sampling gate? Give the applications of sampling gate?

## PART -B

2 a) Discuss about attenuators.
b) Analyze the low pass circuit for the exponential inputs, with help of waveforms.
c) Discuss the application of an attenuator in a CRO.

3 a) Draw the basic circuit diagram of positive peak clamper circuit and explain its operation.
b) Explain transfer characteristics of emitter coupled clipper and derive necessary equations.

4 a) Draw the circuits of 3-input OR-gate using diodes for:
(i) Positive logic, (ii) Negative logic and explain the operation of circuit.
b) Give the comparison of different logic families.

5 a) What are different types of multivibrators? Explain the stable state of a multivibrator.
b) Sketch the circuit diagram of Schmitt trigger and explain its operation.

6 a) With the help of neat circuit diagram and waveforms explain transistor miller time base generator.
b) Discuss about the recovery time of a sweep circuit. How do you achieve short [8M] recovery time?

7 a) Explain the process of synchronization of a sweep circuit.
b) Write notes on:
i) Astable relaxation circuits ii) Monostable relaxation circuits

